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🔍 Title: **CA2265692AA: CODEUR / DECODEUR DE PROTOCOLES DE RESE,  
MULTIPLES ET UNITE DE TRAITEMENT DE DONNEES**

🔍 Country: **CA** Canada

🔍 Kind: **AA** Laid-open Application <sup>i</sup> (See also: [CA2265692C](#) )

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🔍 Assignee: **IREADY CORPORATION** United States of America  
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🔍 Published / Filed: **1998-05-07** / 1997-09-26

🔍 Application **CA1997002265692**

Number:

🔍 IPC Code: **H04J 3/16**; **H04J 3/22**;

🔍 ECLA Code: None

🔍 Priority Number: 1996-10-31 **US1996000742085**  
1997-09-26 **WO1997000017257**

🔍 Abstract: A multiple network protocol encoder/decoder comprising a network protocol layer (101), data handler (102), O.S. State machine (104), and memory manager (103) state machines implemented at a hardware gate level. Network packets are received from a physical transport level mechanism by the network protocol layer state machine (101) which decodes network protocols such as TCP, IP, user Data Protocol (UDP), PPP, and Raw Socket concurrently as each byte is received. Each protocol handler parses and strips header information immediately from the packet, requiring no intermediate memory. The resulting data are passed to the data handler (102) which consists of data state machines (104) that decode data formats such as email, graphics, Hypertext Transfer Protocol (HTTP), Java, and Hypertext Markup Language (HTML). Each data state machine (104) reacts accordingly to the pertinent data, and any data that are required by more than one data state machine (104) is provided to each state machine concurrently, and any data required more than once by a specific data state machine, are placed in a specific memory location (206) with a pointer designating such data; thereby ensuring minimal memory usage. Resulting display data are immediately passed to a display controller (205). Any outgoing network packets are created by the data state machines and passed through the network





protocol state machine which adds header information and forwards the resulting network packet via a transport level mechanism.

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Legal Status:

Gazette date	Code	Description (remarks) <small>List all possible codes for CA</small>
2003-04-03	AFNE +	National phase entry ( 1999-03-12 )
2003-04-03	EEER +	Examination request ( 2000-11-30 )

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Family:

PDF	Publication	Pub. Date	Filed	Title
	<a href="#">WO9819412A1</a>	1998-05-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
	<a href="#">US6034963</a>	2000-03-07	1996-10-31	Multiple network protocol encoder/decc data processor
<input checked="" type="checkbox"/>	<a href="#">JP2001503577T2</a>	2001-03-13	1997-09-26	
	<a href="#">EP0935855A4</a>	2000-05-17	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
	<a href="#">EP0935855A1</a>	1999-08-18	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	<a href="#">CN1237295A</a>	1999-12-01	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	<a href="#">CA2265692C</a>	2001-08-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	<a href="#">CA2265692AA</a>	1998-05-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	<a href="#">AU4595297A1</a>	1998-05-22	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	<a href="#">AU0723724B2</a>	2000-09-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
10 family members shown above				

Other Abstract  
Info:

None



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